

**Amendments to the Specification:**

Please amend the specification as follows:

Please replace the paragraph beginning on page 8, line 5, with the following amended paragraph:

Operation of the exemplary enabling circuit 204a in conjunction with the system 200 is described further herein with reference to the Truth Table of Table 1. Table 1 details the status of the various control signals and switches relative to each other with the output decision circuit 234 functioning as the NOR gate 234a of FIG. 2A. The status of control signal chginh input to the enabling circuit 204, control signal dsch output from comparator 232a, control signal chgen output from the NOR gate 234a, switches K1, K2 of the SRC 202, and switches K3, K4 of the enabling circuit ~~[[202]]~~ 204 are all detailed in Table 1.

Please replace the paragraph beginning on page 8, line 18, with the following amended paragraph:

As illustrated and described more fully herein, the exemplary enabling circuit 204a advantageously does not enable switches K1, K2 of the SRC 204 to be controlled by the proper control technique, e.g., PWM control, and maintains switches K1, K2 in an OFF position, until the capacitor 206 of the SRC ~~[[204]]~~ 202 is discharged below a reference charge (control signal dsch is L), and at least one other non capacitor charge related condition for operation of the regulating circuit (control signal chginh is L) is satisfied. As such, negative voltage transients that may otherwise occur at the output terminal 211 of the SRC ~~[[204]]~~ 202 are avoided.

Please replace the paragraph beginning on page 9, line 3, with the following amended paragraph:

As illustrated in the first substantive column of Table 1, if the charge level on capacitor 206 is greater than a reference charge level as represented by control signal [[vdsch]] Vdsch input to the comparator 232a, then the control signal dsch output from the comparator 232a is HIGH. The output control signal dsch from the comparator 232a is then input to the NOR gate 234a.

Please replace the paragraph beginning on page 9, line 7, with the following amended paragraph:

The other input to the NOR gate 234a may be the control signal chginh from a separate source. Such control signal chginh is representative of at least one non capacitor related condition pertinent to operation of the SRC 202. In this embodiment, if this chginh signal is LOW, at least one and perhaps all other non capacitor related conditions pertinent to operation of the SRC 202 are satisfactory. If this chginh signal is HIGH, such condition or conditions are unsatisfactory. Accordingly, if the output control signal dsch from the comparator 232a is HIGH and the chginh signal is also HIGH, the output signal from the NOR gate [[344a]] 234a is LOW. Thus, switches K1 and K2 remain OFF or open and the operation of the SRC 202 is delayed. As such, if the SRC is operating to provide a charging current to an associated rechargeable battery,

such charging current would not be provided in this instance, nor would current flow from the capacitor 206 through the inductor 208 be possible in this instance.

Please replace the paragraph beginning on page 9, line 18, with the following amended paragraph:

Turning to the second substantive column of Table 1, if the capacitor 206 is discharged below the reference charge represented by control signal [[vdsch]] Vdsch, the output control signal dsch from the comparator 232a goes LOW indicating the charge level on the capacitor 206 is acceptable. However, if another non capacitor charge related condition is unsatisfactory, the control signal chginh remains HIGH. As such, the output of the NOR gate 234a remains LOW and the operation of the SRC 202 is still delayed.

Please replace the paragraph beginning on page 10, line 5, with the following amended paragraph:

As illustrated in the fourth substantive column of Table 1, it is not until the capacitor 206 is discharged below the reference charge level (control signal dsch output from the comparator 232a is LOW), and at least one if not all other non-charge related conditions for operation of the regulating circuit (control signal chginh is LOW) are satisfied, that the control signal chgen output of the NOR gate 234a is HIGH. Once the control signal chgen is HIGH, switches K3 and K4 of the enabling circuit [[202]] 204 open or are in an OFF position. The HIGH control signal

chgen enables the controller 220 to drive the SRC 202. Hence, switches K1, K2 are controlled by an appropriate control technique, e.g., PWM.